

About Karunya University

Karunya University, formerly known as Karunya Institute of Technology and Sciences (KITS) was established in 1986 by the founders Late. Dr. D.G.S. Dhinakaran and Dr. Paul Dhinakaran. Making rapid progress ever since, the Institute has now grown into a university winning recognitions and awards on its way: Best Engineering College in Tamilnadu (1996), first-ever autonomous self-financing college (2000), Deemed University (2004) and University (13th October 2006). The Institution has excellent academic, research and extra-curricular facilities effectively utilized by the well-qualified and dedicated faculty and over 7000 students. UG, PG and PhD programs are offered in the fields of Engineering, Technology and Management. The University has been awarded 'A' grade by NAAC.

About the Department

The Department of Electronics and Communication was established in the year 1986. It has been well equipped with highly commendable facilities and is effectively guided by a set of devoted and diligent staff members with the avowed intent of raising highly qualified Engineers and Researchers. The Department has strength of 920 students and 60 staff members. Advanced labs like Signal Processing, Image Processing and VLSI are pride of the Department. The Department has also signed MoUs with companies like Jasmin Infotech, Tata Elxsi, TRI Techno solutions and Ananya innovations private Ltd. The Internal Quality Assessment Cell reviewed and ranked the department **first** for the past two years, 2007-2008, 2008-2009.

Objective of the Workshop

The National workshop on “**ARM Processor**” gives an overview and hands on training of the **KEIL-C and ARM processor**. Keil development tools for the 8051 Microcontroller Architecture support every level of software developer from the professional applications engineer to the student just learning about embedded software development. It gives a complete view of the industry-standard Keil C Compilers, Macro Assemblers, Debuggers, Real-time Kernels, Single-board Computers, and Emulators supporting all 8051 derivatives.

Highlights of the Workshop

- Introduction to 16/32-bit Processors
- **ARM** Architecture & Organization
- **ARM THUMB** Instruction set
- **ARM/THUMB** Programming Model
- **ARM/THUMB** Assembly
- ARM Exception Handling
- **ARM/THUMB** Interworking
- How **RTOS** is different from OS?
- Hands on training in **KEIL C**

Resource Person:

**Mr.B.Karthikeyan &
Mr.Shriram K Vasudevan
Assistant Professor,
Embedded System Division,
VIT University, Vellore.**

Who Can Attend?

- Faculty from Institution who are teaching or proposing to teach Embedded Systems
- Practicing Engineers from the industry
- Students – Graduates and post Graduates

COURSE FEE

For all the Participants : Rs.600/-
(Includes course material and working lunch)

Demand Draft in favour of "**The Registrar, Karunya University**" payable at Coimbatore

DATES TO REMEMBER

Last date for registration : 22.03.2010

Intimation of Selection : 23.03.2010

On the spot registration is allowed with prior intimation.

For accommodation details please contact

Mr.S.Suresh, Lecturer/ECE

Mobile:9894514513

Registration Form

Name :

Department:

Organization :

Experience:

Address for Correspondence:

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Email:

Phone:

Accommodation Required Yes No

Demand Draft Details:

Faculty Student

Amount :

DD No. :

Bank :

Dated :

Signature:

(Use Photocopy of the above form, if required)

ORGANIZING COMMITTEE

Patron

Dr. Paul Dhinakaran
Chancellor, KU

Chairman

Dr. Paul P. Appasamy
Vice Chancellor, KU

Vice Chairman

Dr. Anne Mary Fernandez
Registrar, KU

Convener

Dr. C. Kezi Selva Vijila
HOD, ECE

Co-Convener(s)

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MAILING ADDRESS

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National Workshop On “ARM Processor” 26th & 27th March 2010



Organized by

**Department of
Electronics and Communication
Engineering**

**Karunya School of Electrical Sciences
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Coimbatore-641114
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